

FIG. 1

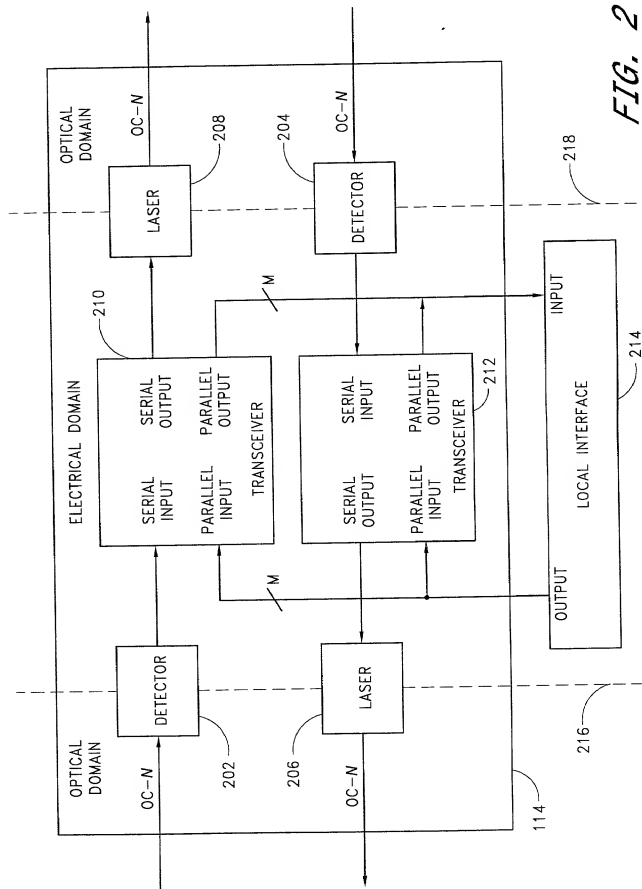


FIG. 2

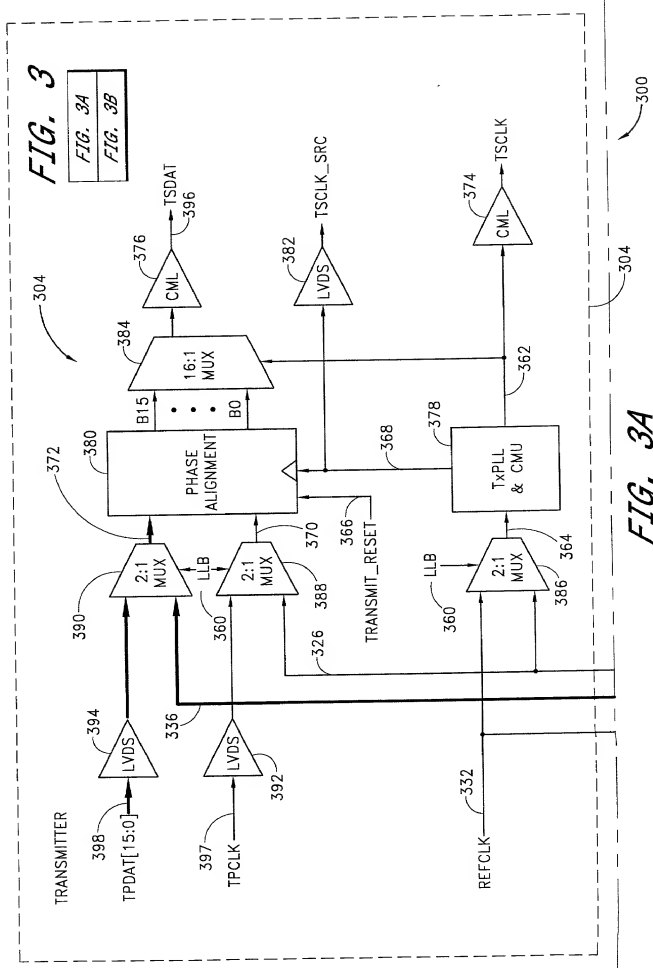


FIG. 3A

FIG. 3

FIG. 3A
FIG. 3B

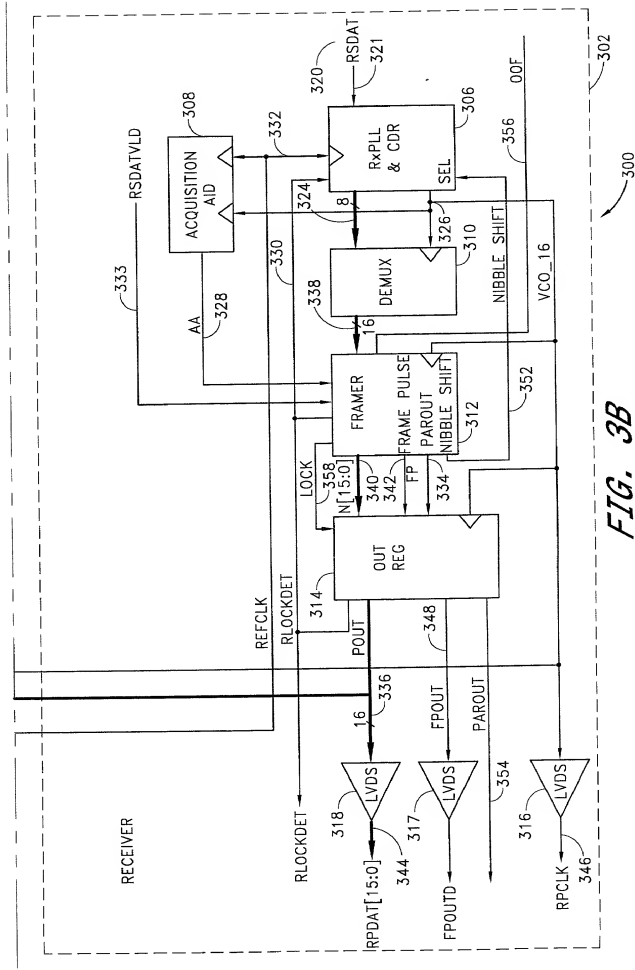


FIG. 3B

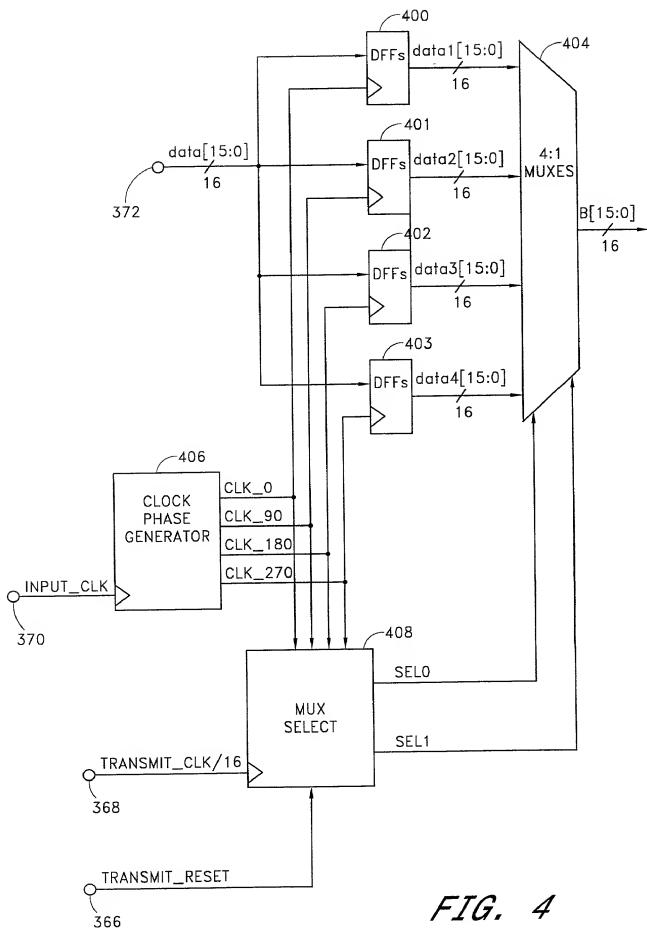


FIG. 4

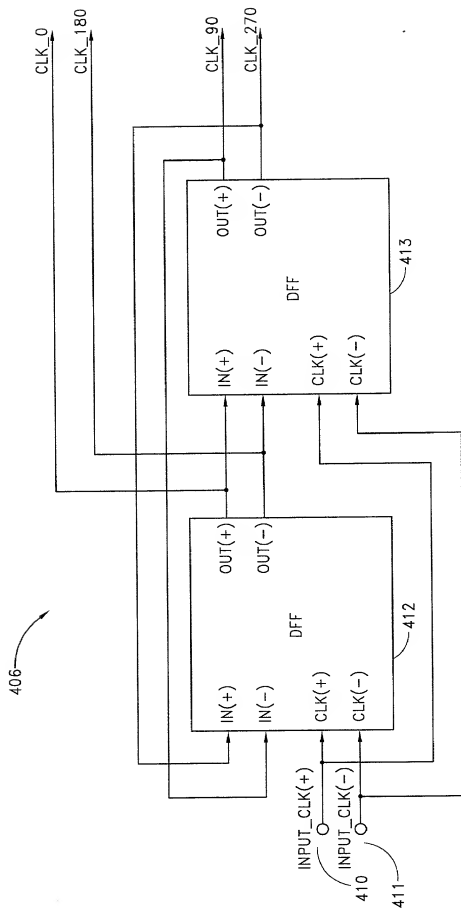
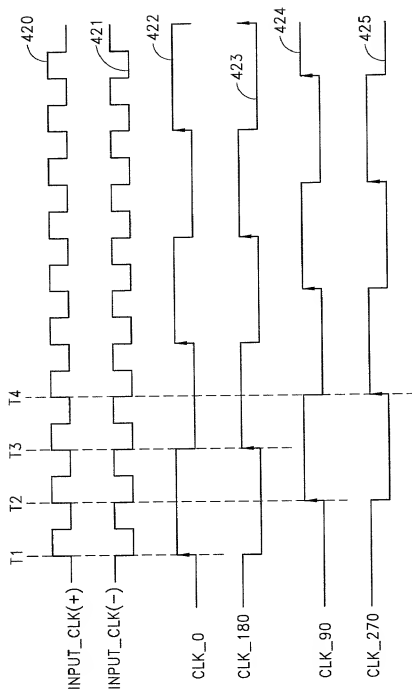


FIG. 5



*FIG. 6*

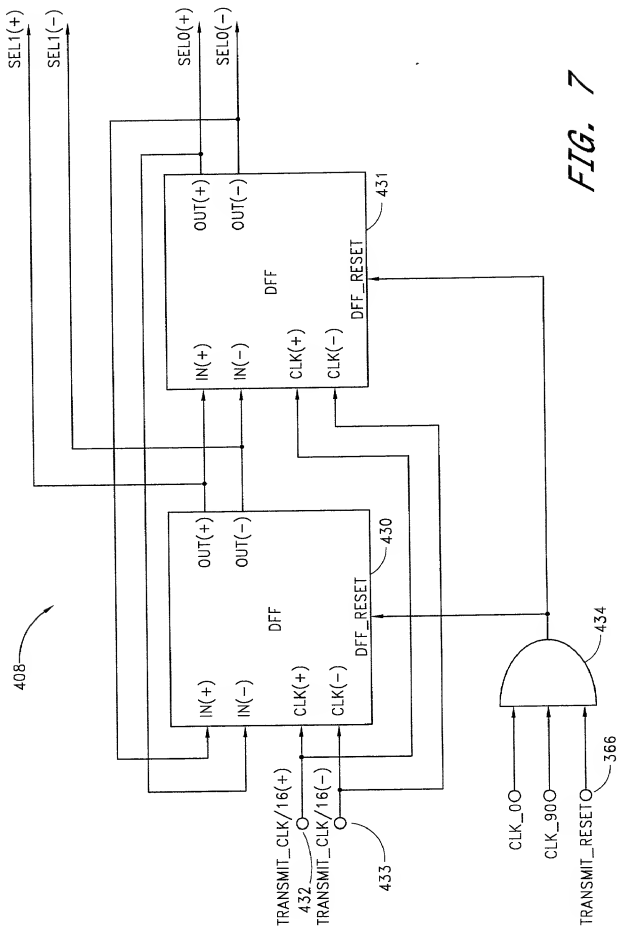


FIG. 7



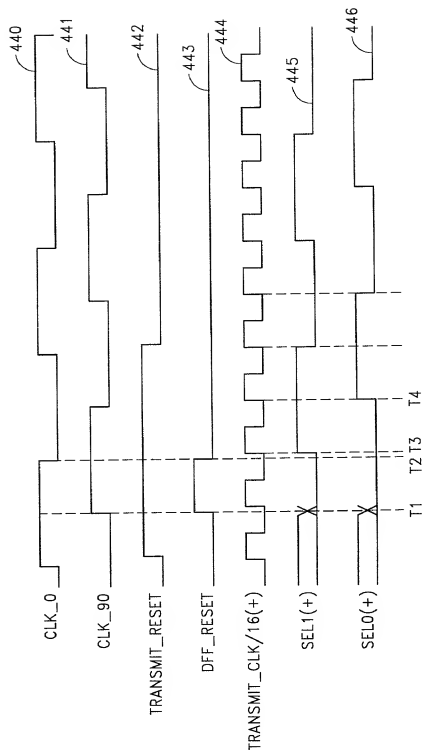


FIG. 8

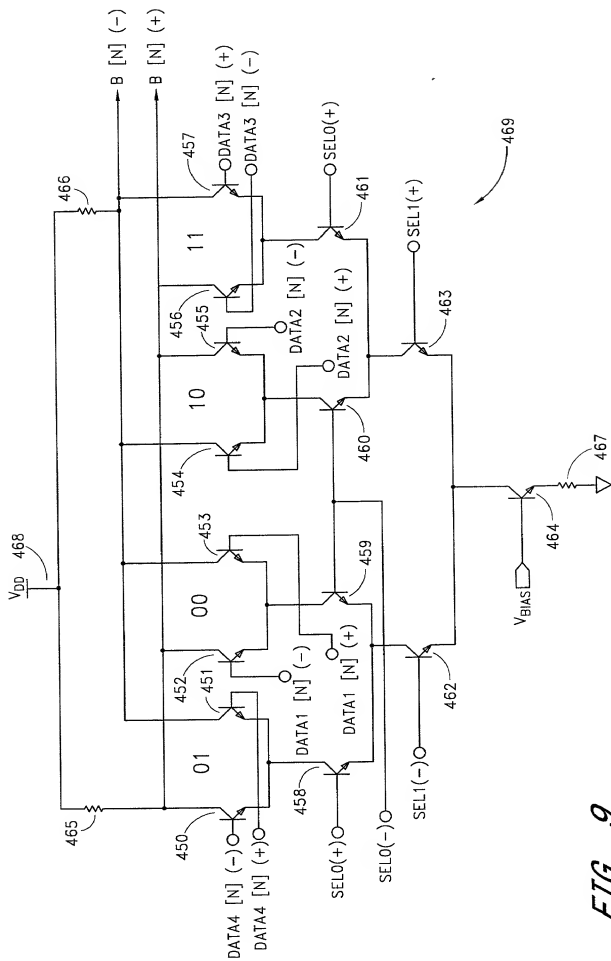


FIG. 9

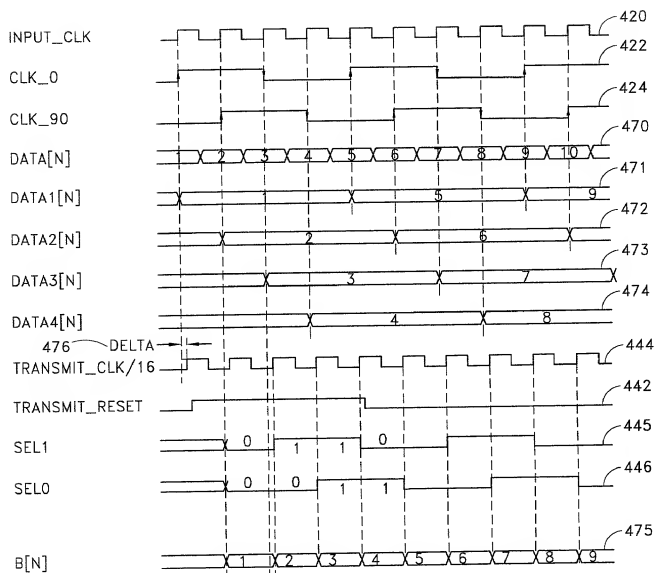
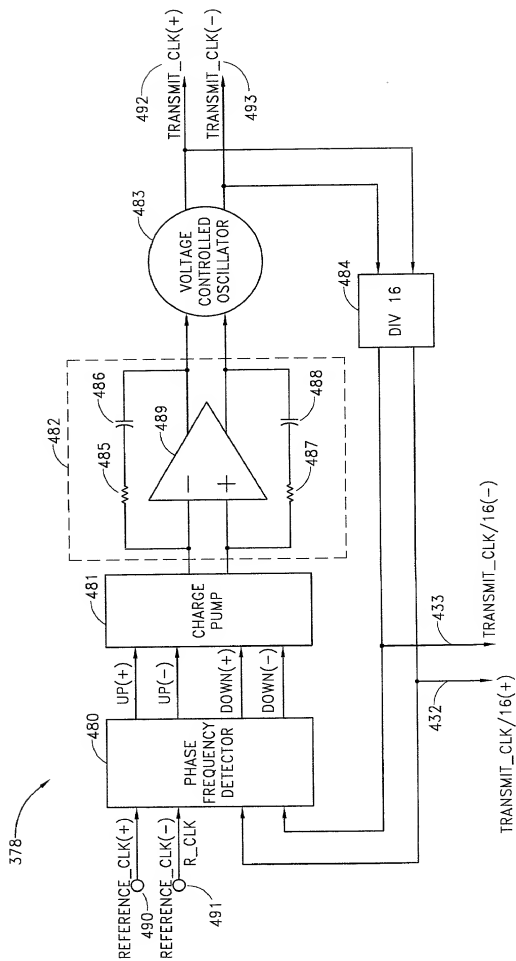


FIG. 10



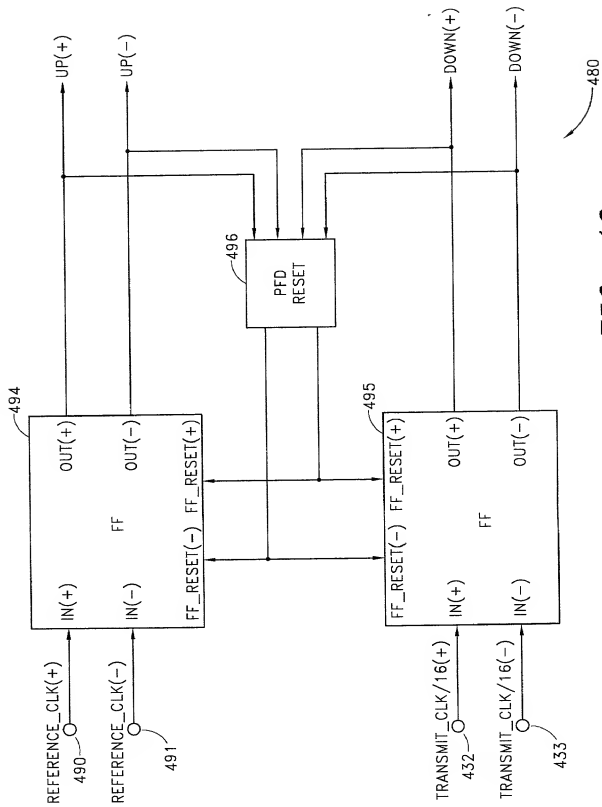


FIG. 12

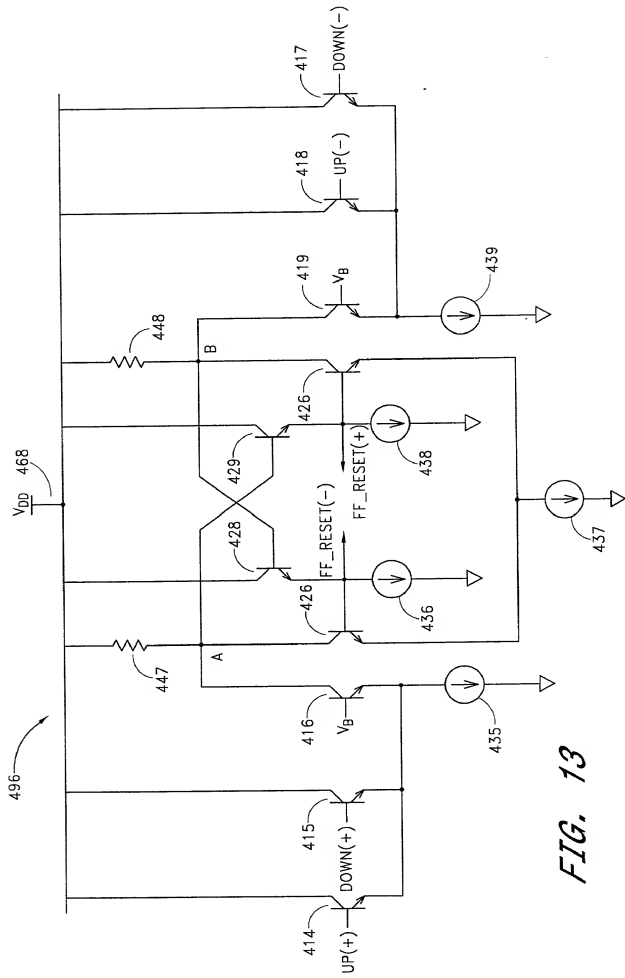


FIG. 13

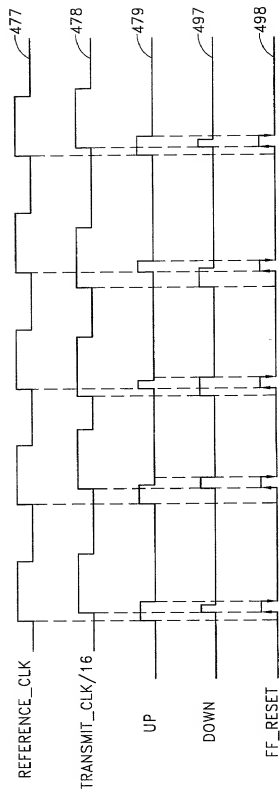


FIG. 14

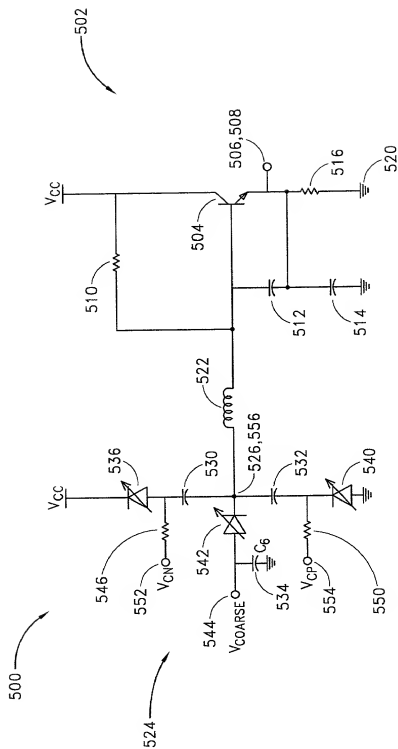


FIG. 15



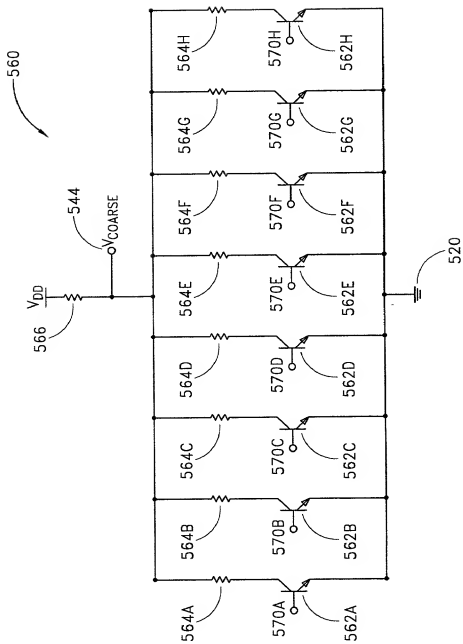


FIG. 16

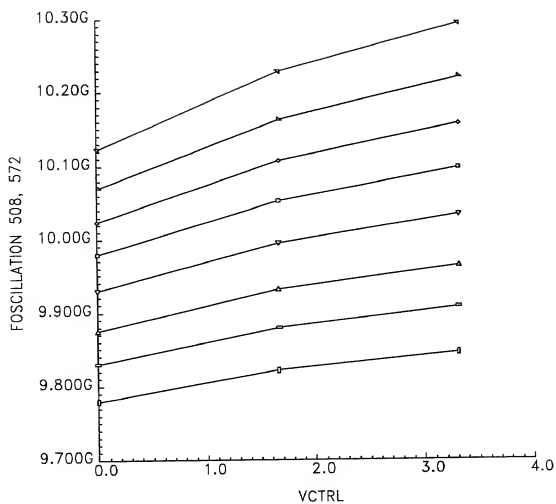


FIG. 17

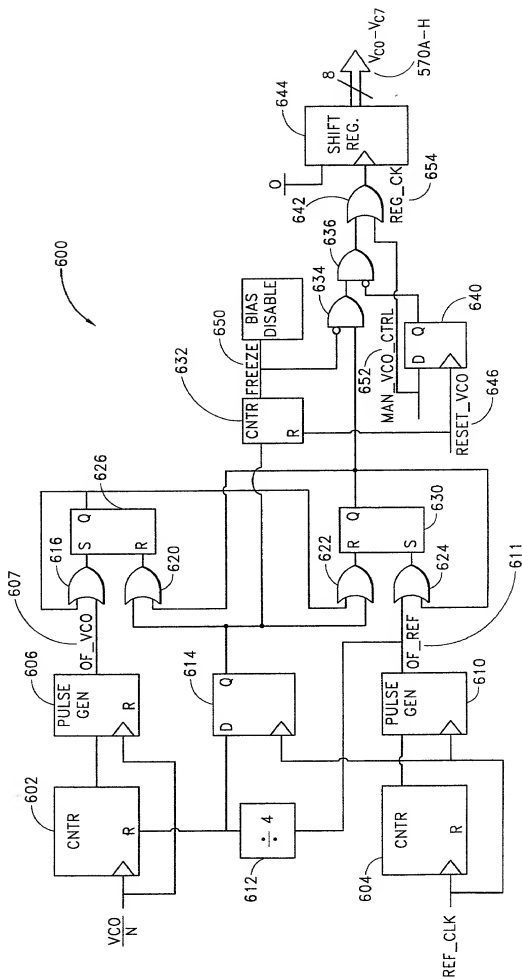


FIG. 18

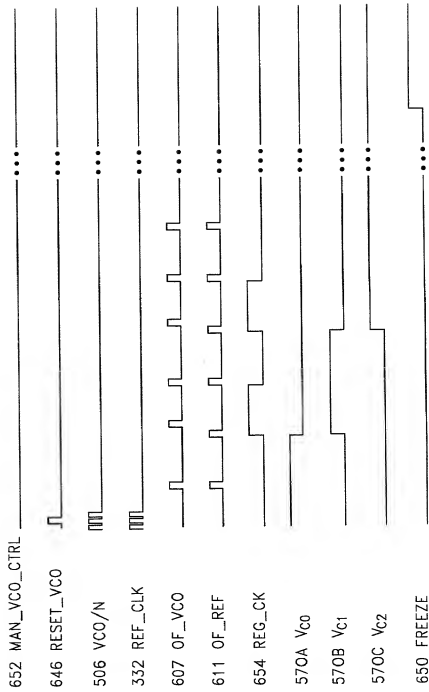


FIG. 19

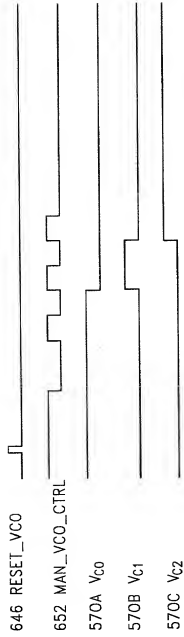


FIG. 20

```

graph TD
    702([RESET_VCO  
646 ACTIVE]) --> 704{FREEZE 650  
ACTIVE ?}
    704 --> 706[MAINTAIN CURRENT  
VCOARSE 544 AND  
DISABLE DIGITAL  
SEARCH FILTER 600]
    704 --> 710{MAN_VCO_CTRL  
652 ACTIVE ?}
    710 -- YES --> 750[INCREMENT  
VCOARSE 544]
    710 -- NO --> 700[CONDUCT AND MONITOR  
RACE BETWEEN  
REF_CLK 332  
AND VCO OUTPUT 506]
    700 --> 712{REF_CLK 332  
FASTER THAN VCO  
OUTPUT FREQUENCY 508  
DIVIDED BY 16 ?}
    712 -- YES --> 714[INCREMENT  
VCOARSE 544]
    712 -- NO --> 716([MAINTAIN CURRENT  
VCOARSE 544])
    714 --> 710
    750 --> 710
  
```

FIG. 21

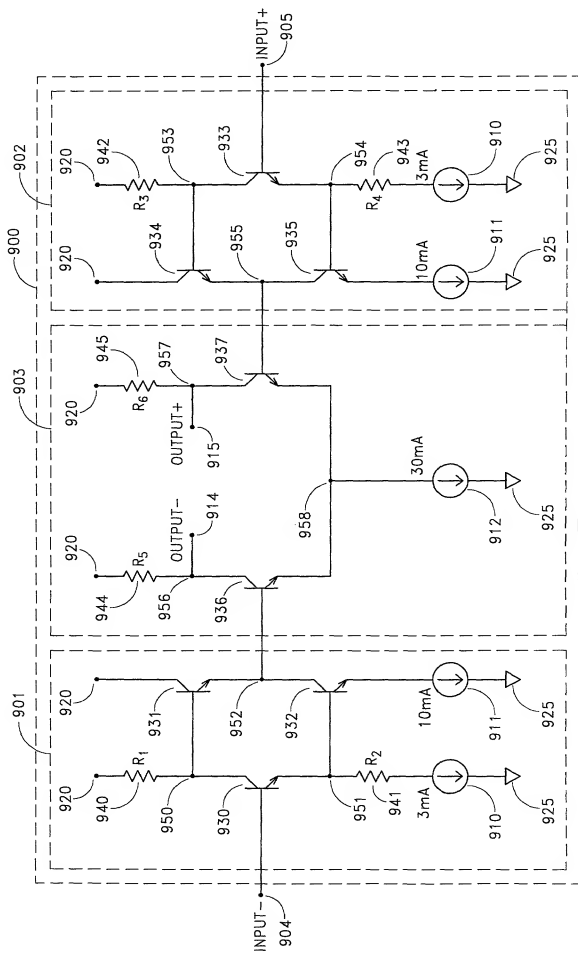


FIG. 22